

DATE: 22/02/2022

Event Coordinator(s)

Meena Ugale

Sayali Mane

Time & Place:

22/02/2022

(02:15-03:45 PM)

Online session on Google meet,

Xavier Institute of
Engineering,

Mahim, Mumbai

Department:

Information Technology

Computer Engineering

No of participants: 110

EVENT: Guest Lecture on Multicore Architecture

Resource Person: Ms. Garima Tripathi, ME (Electronics), Assistant Professor, FrCRCE, Bandra(W), Mumbai.

Target audience: SE-IT, SE-COMPS students

This session was conducted on Google Meet online platform and 110 participants attended it.

Objectives of the session:

1. To know the recent advancement in processor design.
2. To understand the multi core architecture.
3. To understand what happens in network on chip.

Topics Covered:

1. What is Multicore?
2. What is NoC?
3. Role of memory controller
4. Routing Topologies

Student Feedback and Benefits:

- Students found this session very informative & interesting
- Very informative session
- It was really something new and interesting!!

Meena Ugale
Asst.Prof. and HOD I/C
(IT Dept)

Sayali Mane
Asst.Prof.
(COMPS Dept)

Images

MULTI-CORE ARCHITECTURE

Garima Tripathi
Fr.C.R.C.E

Meena Ugale, ANISHA Prabhu, SUDEEP Poogary, ANNE NELSON, FALGUNI Joshi, ADITYA POLE, 30 others, You

dey-ihdv-ash

This screenshot shows a Zoom meeting interface. The main window displays a slide titled "MULTI-CORE ARCHITECTURE" by Garima Tripathi, Fr.C.R.C.E. The slide content is mostly blank. To the right is a grid of participant avatars, including Meena Ugale, ANISHA Prabhu, SUDEEP Poogary, ANNE NELSON, FALGUNI Joshi, ADITYA POLE, and a group of 30 others. The bottom bar shows standard Zoom controls and the meeting ID "dey-ihdv-ash".

WHY MULTICORE?

Impact of VLSI in processor trends

Meena Ugale, ANISHA Prabhu, SUDEEP Poogary, ANNE NELSON, FALGUNI Joshi, ADITYA POLE, 30 others, You

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This screenshot shows a Zoom meeting interface. The main window displays a slide titled "WHY MULTICORE?" with a sub-heading "Impact of VLSI in processor trends". The slide features a line graph showing an upward trend in processor performance over time, with a small inset image of two people. To the right is a grid of participant avatars, including Meena Ugale, ANISHA Prabhu, SUDEEP Poogary, ANNE NELSON, FALGUNI Joshi, ADITYA POLE, and a group of 30 others. The bottom bar shows standard Zoom controls and the meeting ID "dey-ihdv-ash".

WHY MULTICORE?

Impact of VLSI in processor trends

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